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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Fred D. Bailey et al. Art Unit: 2815
Serial No.: 09/452,691 Examiner: J. A. Fenty
Filing Date: 12/02/1999 Docket No.: TI-27935
Customer No.: 23494 Conf. No.: 1638
Title: AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED
WITHIN A MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER METAL
INTERCONNECT LAYER

CERTIFICATION OF FACSIMILE TRANSMISSION

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Jacqueline J. Garner, Reg. No. 36,144January 24, 2005
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FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT (5 Pages)
<input type="checkbox"/> NEW APPLICATION	<input checked="" type="checkbox"/> EOT 1mth (2 Pages)
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Bailey	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/452,691 Filing Date: 12/02/1999 Conf. No.: 1638	
TITLE OF INVENTION: An Integrated Circuit Having . . .	
TI FILE NO.: TI-27935	DEPOSIT ACCT. NO.: 20-0668
FAXED: 1/24/2005 DUE: 12/22/2004 ATTY/SEC'Y: JJG	

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Texas Instruments Incorporated
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JAN 24 2005

Response under 37 CFR 1.116
Examining Group 2815

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AMENDMENT AFTER FINAL UNDER 37 CFR §1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Jacqueline J. Garner
Reg. No. 36,144

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 09/22/2004. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

There are no **Amendments to the Specification**.**Amendments to the Claims** are reflected in the listing of claims that begin on page 2 of this paper.There are no **Amendments to the Drawings**.The **Remarks** begin on page 4.